IN THE UNITED STATES PATENT OFFICE

Applicant:	Robert O. Conn		
Assignee:	Xilinx, Inc.		
Title:	"Bond and Back Side Etchback Transistor Fabrication Process"		
Serial No.:	Not Yet Known	File Date:	April 14, 2004
Examiner:	Not Yet Known	Art Unit:	Not Yet Known
Divisional of Serial No.:	10/407,746	File Date:	April 4, 2003
Docket No.:	X-1322-1-1D US		

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

PRIORITY INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant brings to the attention of the Examiner the twelve (12) references listed in the attached Substitute for Form PTO-1449 (Information Disclosure Statement by Applicant).

All of these references were cited in prior related U.S. patent application Serial Number 10/407,746 filed April 4, 2003 to which this application claims priority. Copies of these references have not been supplied herein since they were previously submitted in the parent case.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully Submitted,

Justin Liu
Attorney for Applicant
Reg. No. 51,959

JL:jam

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PTO/SB/08A (10-01)
Approved for use through 10/31/2002. OMB 0651-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE
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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 2

Complete if Known	
Application / Conf. No.	Unknown /
Filing Date	April 14, 2004
First Named Inventor	Robert O. Conn
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	X-1322-1-1D US

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No.[1]	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 5,436,173	07-25-95	Houston	
		US- 5,508,219	04-16-96	Bronner et al.	
		US- 5,591,678	01-07-97	Bendik et al.	
		US- 5,767,549	06-16-98	Chen et al.	
		US- 6,063,686	05-16-00	Masuda et al.	
		US- 6,420,218 B1	07-16-02	Yu	
		US- 6,518,613 B2	02-11-03	Willer et al.	
		US- 6,521,947 B1	02-18-03	Ajmera et al.	
		US- 10/405,789		Conn	(filed 4/1/03)
		US- 5,354,695	10-11-94	Leedy	
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FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No.[1]	Foreign Patent Document Country Code [3] -Number [4] -Kind Code [5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T [6]

Examiner Signature		Date Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1] Applicant's unique citation designation number (optional). [2] See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. [3] Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). [4] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [5] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. [6] Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

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Application / Conf. No.	Unknown /
Filing Date	April 14, 2004
First Named Inventor	Robert O. Conn
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	X-1322-1-1D US

OTHER -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
		Q.-Y. TONG, "Semiconductor Wafter Bonding: Science And Technology," 1999, pages 229-231, 235-237 & 284-285, available from John Wiley & Sons, Inc., 605 Third Ave, New York, N.Y. 10158-0012	
		ALEX ROMANELLI, "Intel Stacks Flash Deck in its Favor," Electronic News, 4/10/2003, available from Reed Electronics Group @ http://www.e-insite.net/electronicnews/index.asp?layout=article&articleid=CA291318.	

Examiner Signature		Date Considered	

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